

JP973 U.S. PTO
10/06/1492
01/31/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10061492	01/31/2002	083	216	1763	R. Culbert

****APPLICANTS:** Buswell Shen; Rivas Rio; Khavari Mehrgan; Templin Paul; MacKenzie
Mark; Jensen Conrad;
See updated
Bib Sheet

****CONTINUING DATA VERIFIED:** *M*

**** FOREIGN APPLICATIONS VERIFIED:** *M*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's Initials <i>M</i>		ATTORNEY DOCKET NO 10011880-1

TITLE : Methods and systems for forming slots in a semiconductor substrate

U.S. DEPT. OF COMM./PAT. & TM-PTO-438L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE Amount Due Date Paid		DRAWING Sheets Drwg. Figs. Drwg. Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

BEST AVAILABLE COPY FILED WITH: ☐ DISK (CRF) ☐ CD-ROM
(Attached in pocket on right inside flap)